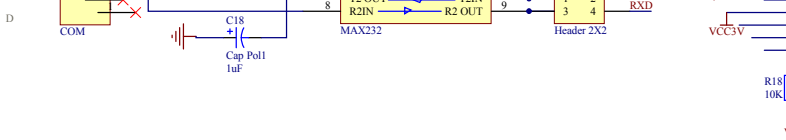
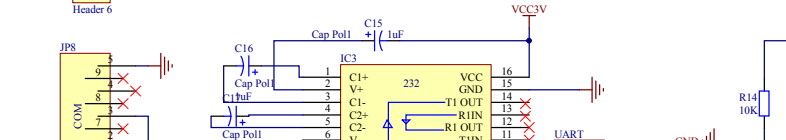
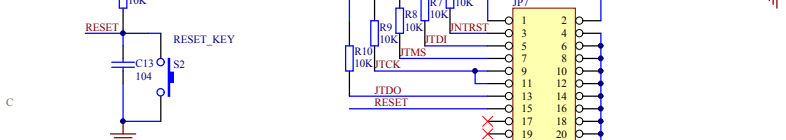
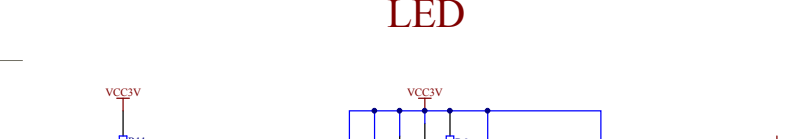
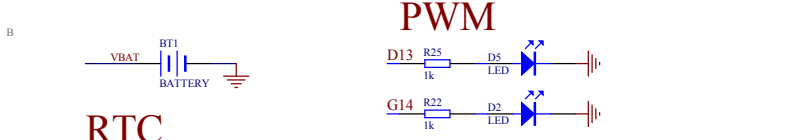
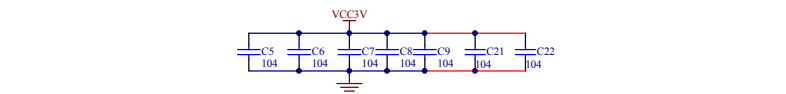
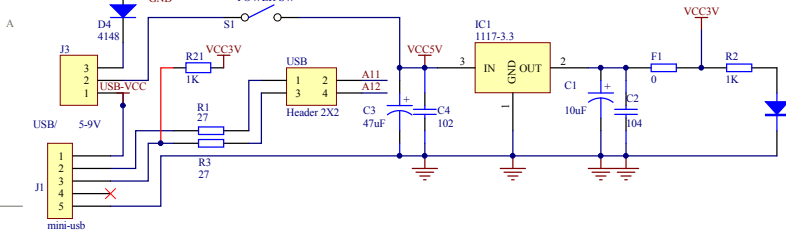
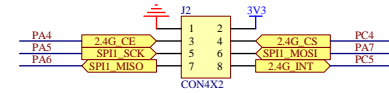
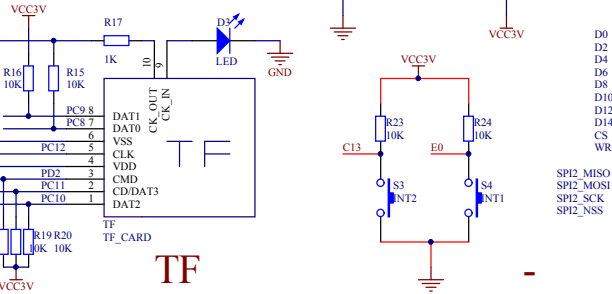
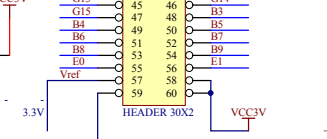
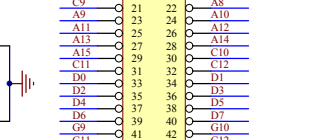
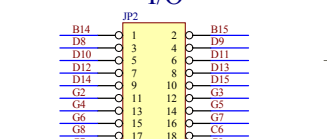
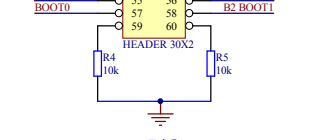
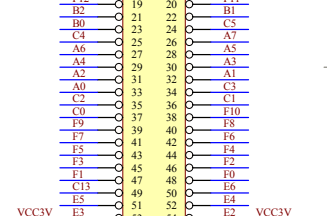
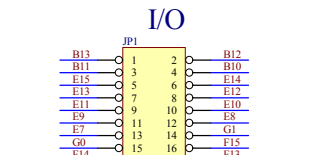


STM32F103ZET6



IC2	STM32F103ZET6	NC	106	X
A0	34	PA0/WKUP/USART2_CTS/ADC123_IN0/TIM5_CH1/TIM2_CH1_ETR/TIM8_ETR	141	E0
A1	35	PA1/USART2_RTS/ADC123_IN1/TIM5_CH2/TIM2_CH2	142	E1
A2	36	PA2/USART2_TX/TIM5_CH3/ADC123_IN2/TIM2_CH3	143	E2
A3	37	PA3/USART2_RX/TIM5_CH4/ADC123_IN3/TIM2_CH4	144	E3
A4	40	PA4/SPI1_NSS/DAC_OUT1/USART2_CK/ADC12_IN4	145	E4
A5	41	PA5/SPI1_SCK/DAC_OUT2/ADC12_IN5	146	E5
A6	42	PA6/SPI1_MISO/TIM8_BKIN/ADC12_IN6/TIM3_CH1	147	E6
A7	43	PA7/SPI1_MOSI/TIM8_CH1N/ADC12_IN7/TIM3_CH2	148	E7
A8	100	PA8/USART1_CK/TIM1_CH1/MCO	149	E8
A9	101	PA9/USART1_TX/TIM1_TX/TIM1_CH2	150	E9
A10	102	PA10/USART1_RX/TIM1_CH3	151	E10
A11	103	PA11/USART1_CTS/CANRX/TIM1_CH4/USBDM	152	E11
A12	104	PA12/USART1_RTS/CANTX/TIM1_ETR/USBDP	153	E12
A13	105	PA13/TIM8_SWDO	154	E13
A14	109	PA14/JTCK-SWCLK	155	E14
A15	110	PA15/JTDI/SPI3_NSS/I2S3_WS	156	E15
B0	46	PB0/ADC12_IN8/TIM3_CH3/TIM8_CH2N	157	E16
B1	47	PB1/ADC12_IN9/TIM3_CH4/TIM8_CH3N	158	E17
B2	48	PB2/BOOT1	159	E18
B3	133	PB3/JTDO/TRACESWO/SPI3_SCK/I2S3_CK	160	E19
B4	134	PB4/INTRST/SPI3_MISO	161	E20
B5	135	PB5/I2C1_SMBAL/SPI3_MOSI/I2S3_SD	162	E21
B6	136	PB6/I2C1_SCL/TIM4_CH1	163	E22
B7	137	PB7/I2C1_SDA/FSMC_NADV/TIM4_CH2	164	E23
B8	139	PB8/TIM4_CH3/SPIO_D4	165	E24
B9	140	PB9/TIM4_CH4/SPIO_D5	166	E25
B10	69	PB10/I2C2_SCL/USART3_TX	167	E26
B11	70	PB11/I2C2_SDA/USART3_RX	168	E27
B12	73	PB12/SPI2_NSS/I2S2_WS/I2C2_SMBAL/USART3_CK/TIM1_BKIN	169	E28
B13	74	PB13/SPI2_SCK/I2S2_CS/USART3_CTS/TIM1_CH1N	170	E29
B14	75	PB14/SPI2_MISO/USART3_RTS/TIM1_CH2N	171	E30
B15	76	PB15/SPI2_MOSI/I2S2_SD/TIM1_CH3N	172	E31
C0	26	PC0/ADC12_IN10	173	E32
C1	27	PC1/ADC12_IN11	174	E33
C2	28	PC2/ADC12_IN12	175	E34
C3	29	PC3/ADC12_IN13	176	E35
C4	44	PC4/ADC12_IN14	177	E36
C5	45	PC5/ADC12_IN15	178	E37
C6	96	PC6/I2S2_MCK/TIM8_CH1/SPIO_D6	179	E38
C7	97	PC7/I2S2_MCK/TIM8_CH2/SPIO_D7	180	E39
C8	98	PC8/TIM8_CH3/SPIO_D0	181	E40
C9	99	PC9/TIM8_CH4/SPIO_D1	182	E41
C10	111	PC10/USART4_TX/SPIO_D2	183	E42
C11	112	PC11/USART4_RX/SPIO_D3	184	E43
C12	113	PC12/USART5_TX/SPIO_CK	185	E44
C13	7	PC13-TAMPER-RTC	186	E45
C14	8	PC14-OSC32_IN	187	E46
C15	9	PC15-OSC32_OUT	188	E47
D0	114	PD0/FSMC_D2	189	E48
D1	115	PD1/FSMC_D3	190	E49
D2	116	PD2/TIM3_ETR/USART5_RX/SPIO_CMD	191	E50
D3	117	PD3/FSMC_CLK	192	E51
D4	118	PD4/FSMC_NOE	193	E52
D5	119	PD5/FSMC_NWE	194	E53
D6	122	PD6/FSMC_NWAIT	195	E54
D7	123	PD7/FSMC_NE1/FSMC_NCE2	196	E55
D8	77	PD8/FSMC_D13	197	E56
D9	78	PD9/FSMC_D14	198	E57
D10	79	PD10/FSMC_D15	199	E58
D11	80	PD11/FSMC_A16	200	E59
D12	81	PD12/FSMC_A17	201	E60
D13	82	PD13/FSMC_A18	202	E61
D14	85	PD14/FSMC_D0	203	E62
D15	86	PD15/FSMC_D1	204	E63
BOOT0	138	BOOT0	205	E64



Size	Number	Revision
A4		

Date: 2013-7-19
File: E:\Electronics\Small System V5.SchDoc
Sheet of: 6
Drawn By: